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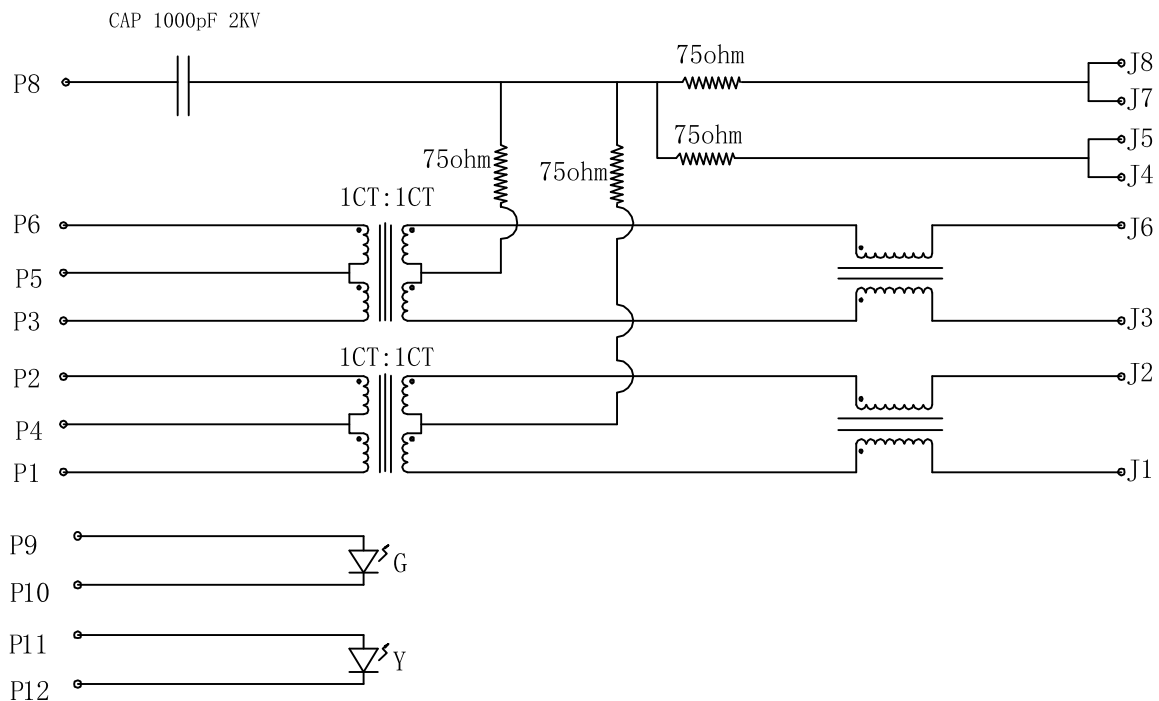
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APPROVED: _____

DATE: _____ TITLE: _____

Rev	AWO #	Description	Date	Appr
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SEE SHT 1 OF 2 FOR REVISION



ELECTRICAL SPECIFICATIONS

- Turns Ratio: (P1 ~ P2) : (J1 ~ J2) 1:1 ±3%
(P3 ~ P6) : (J3 ~ J6) 1:1 ±3%
- Inductance: (P1 ~ P2) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
(P3 ~ P6) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P1 ~ P2 (with J1 and J2 Short): 0.35uH Max. @ 1 MHz
P3 ~ P6 (with J3 and J6 Short): 0.35uH Max. @ 1 MHz
- Interwinding Capacitor: (P1 ~ P2) : (J1 ~ J2): 35pF max. @ 1MHZ
(P3 ~ P6) : (J3 ~ J6): 35pF max @ 1mhz
- DC Resistance: (J1 ~ J2) : (J3 ~ J6): 1.2 Ohms Max.
- Return Loss: 1MHZ to 30MHZ: -18dB MIN
30MHZ to 60MHZ: -(18-20LOG(f/30))dB MIN (WHERE f IS IN MHZ)
60MHZ to 80MHZ: -12dB MIN
- Dielectric Withstanding: (P1~P2) : (J1~J2): (P3~P4) : (J3~J6) : 1500VAC or 2250VDC
- Insertion Loss: 1MHZ TO 100MHZ : -1.2dB MAX
- Cross Talk: 1MHZ to 100MHZ: -30dB MIN
- Common to Common mode attenuaton: 1MHZ TO 100MHZ: -30dBMIN

UNLESS OTHERWISE SPECIFIED
ALL DIMENSIONS ARE INCHES [MM]
TOLERANCES: EXCEPT AS NOTED
INCHES HOLES

.X ± .10	Ø.X ± .10
.XX ± .020	Ø.XX ± .015
.XXX ± .015	Ø.XXX ± .010

ADAM TECH

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TITLE
MODULAR JACK GANGED, 2 PORT, 8P8C
W/ LED1 GREEN, LED2 YELLOW

APPROVALS		DATE
DRAWN	SS	3/4/10
CHECKED		
APPROVED		

SIZE	PART NO.	REV.
X	MTJG-2-88TX1-FSD-PG-LH-M4	B
REF: S00005C	SCALE: NTS	SHEET 1 OF 2

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