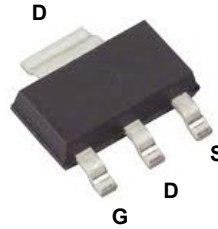
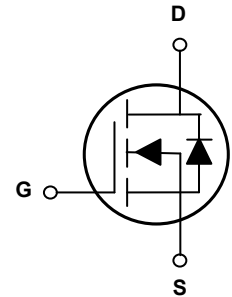


**Main Product Characteristics**

$V_{(BR)DSS}$	650V
$R_{DS(ON)}$	0.78Ω (Typ.)
$I_D$	5A



SOT-223



Schematic Diagram

**Features and Benefits**

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery

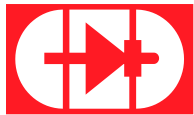


**Description**

The GSFL65R900 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supply and a wide variety of other applications.

**Absolute Maximum Ratings** ( $T_A=25^{\circ}C$  unless otherwise specified)

Parameter	Symbol	Parameter.	Unit
Drain-Source Voltage	$V_{DS}$	650	V
Gate-to-Source Voltage	$V_{GS}$	±30	V
Continuous Drain Current, @ Steady-State ( $T_C=25^{\circ}C$ )	$I_D$	5	A
Continuous Drain Current, @ Steady-State ( $T_C=100^{\circ}C$ )		3.2	A
Pulsed Drain Current	$I_{DM}$	20	A
Power Dissipation ( $T_C=25^{\circ}C$ )	$P_D$	20	W
		0.16	W/°C
Single Pulse Avalanche Energy <sup>1</sup>	$E_{AS}$	214	mJ
Single Pulse Avalanche Current	$I_{AS}$	2.8	A
Body Diode Reverse Voltage Slope <sup>2</sup>	$dv/dt$	15	V/ns
MOS $dv/dt$ Ruggedness <sup>3</sup>	$dv/dt$	50	V/ns
Thermal Resistance, Junction-to-Ambient (PCB Mounted, Steady-State)	$R_{\theta JA}$	62.0	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	6.25	°C/W
Operating Junction and Storage Temperature Range	$T_J/T_{STG}$	-55 to +150	°C
Soldering Temperature	$T_{sold}$	260	°C

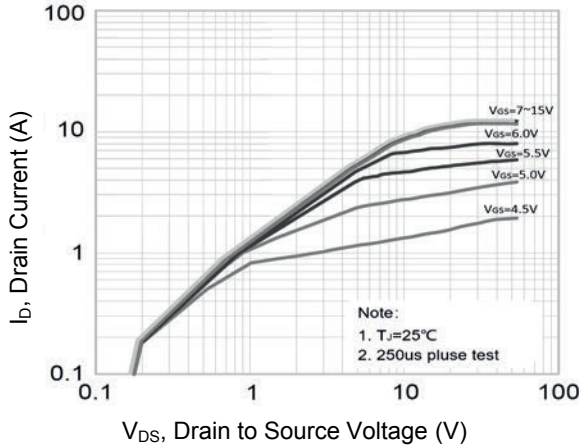

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>On / Off Characteristics</b>						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	650	-	-	V
Drain-to-Source Leakage Current	$I_{DSS}$	$V_{DS}=650V, V_{GS}=0V, T_J=25^\circ\text{C}$	-	-	1.0	$\mu A$
		$V_{DS}=650V, V_{GS}=0V, T_J=125^\circ\text{C}$	-	1.5	-	$\mu A$
Gate-to-Source Forward Leakage	$I_{GSS}$	$V_{DS}=0V, V_{GS}=30V$	-	-	100	nA
		$V_{DS}=0V, V_{GS}=-30V$	-	-	-100	
Static Drain-to-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=2.5A$	-	0.78	0.90	$\Omega$
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	-	4.0	V
<b>Dynamic and Switching Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{GS}=0V, V_{DS}=100V, f=1\text{MHz}$	-	300	-	$\mu F$
Output Capacitance	$C_{oss}$		-	20	-	
Reverse Transfer Capacitance	$C_{rss}$		-	2.4	-	
Total Gate Charge <sup>4,5</sup>	$Q_g$	$I_D=5A, V_{DD}=520V, V_{GS}=10V$	-	13	-	nC
Gate-to-Source Charge <sup>4,5</sup>	$Q_{gs}$		-	3.0	-	
Gate-to-Drain ("Miller") Charge <sup>4,5</sup>	$Q_{gd}$		-	6.8	-	
Gate Plateau <sup>4,5</sup>	$V_{plateau}$		-	6.5	-	
Turn-on Delay Time <sup>4,5</sup>	$t_{d(on)}$	$V_{DD}=325V, V_{GS}=10V, R_G=24\Omega, I_D=5A$	-	8.7	-	nS
Rise Time <sup>4,5</sup>	$t_r$		-	25	-	
Turn-Off Delay Time <sup>4,5</sup>	$t_{d(off)}$		-	30	-	
Fall Time <sup>4,5</sup>	$t_f$		-	23	-	
Gate Resistance	$R_g$	$f=1\text{MHz}$	-	7.2	-	$\Omega$
<b>Source-Drain Ratings and Characteristics</b>						
Continuous Source Current (Body Diode)	$I_S$	$T_C=25^\circ\text{C}$ , MOSFET symbol showing the integral reverse p-n junction diode.	-	-	5	A
Diode Pulse Current	$I_{S, pulse}$		-	-	20	A
Diode Forward Voltage	$V_{SD}$	$I_S=5A, V_{GS}=0V$	-	-	1.4	V
Reverse Recovery Time <sup>4</sup>	$T_{rr}$	$I_S=5A, V_{GS}=0V, di_f/dt=100A/\mu s$	-	334	-	nS
Reverse Recovery Charge <sup>4</sup>	$Q_{rr}$		-	2.2	-	$\mu C$

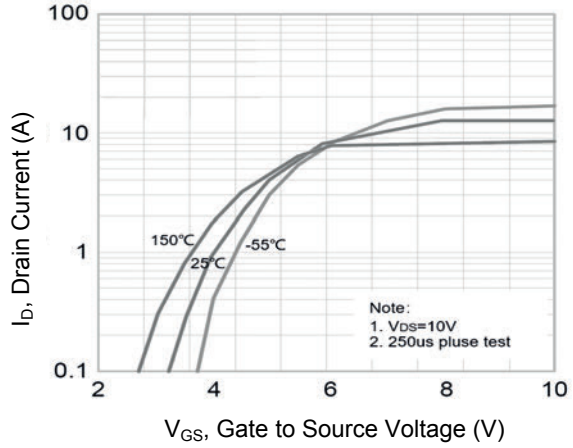
## Notes:

1.  $L=79\text{mH}, V_{DD}=100V, R_G=25\Omega$ , starting temperature  $T_J=25^\circ\text{C}$ .
2.  $V_{DS}=0-400V, I_{SD}\leq I_S, T_J=25^\circ\text{C}$ .
3.  $V_{DS}=0-480V$ .
4. Pulse test: pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
5. Essentially independent of operating temperature.

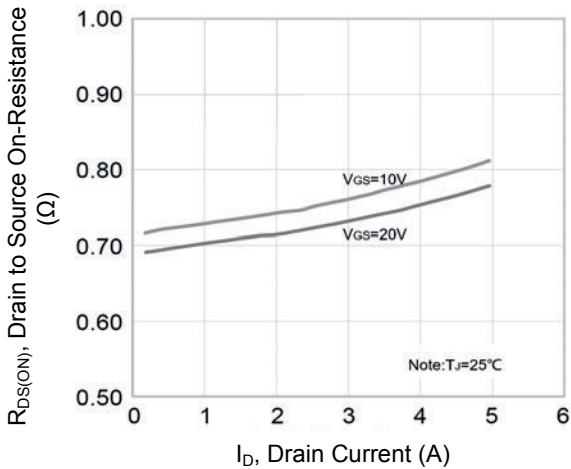
**Typical Electrical and Thermal Characteristic Curves**



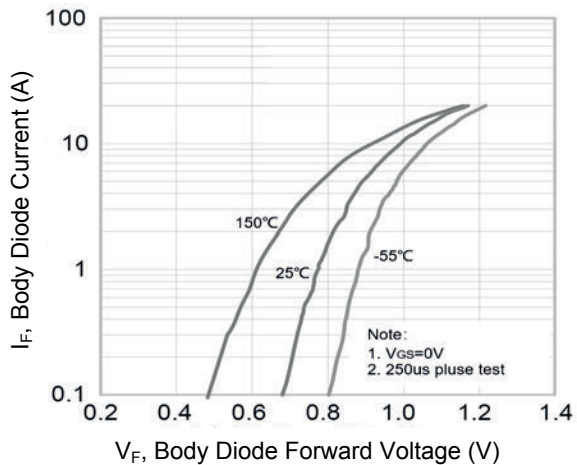
**Figure 1. Typical Output Characteristics**



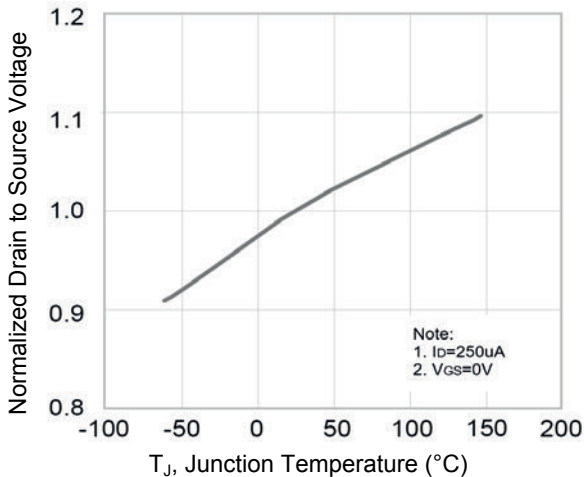
**Figure 2. Transfer Characteristics**



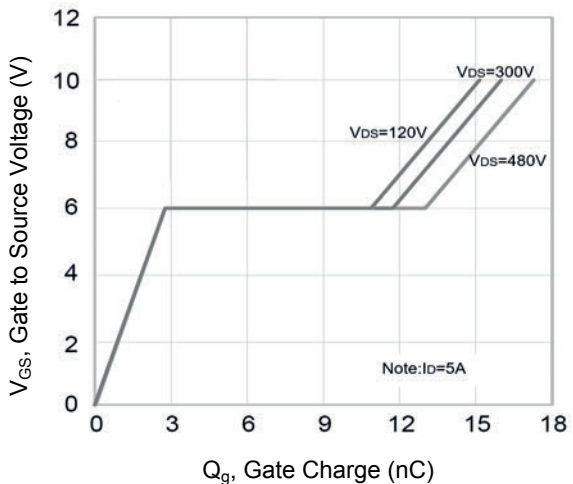
**Figure 3.  $R_{DS(ON)}$  vs. Drain Current**



**Figure 4. Body Diode Characteristics**

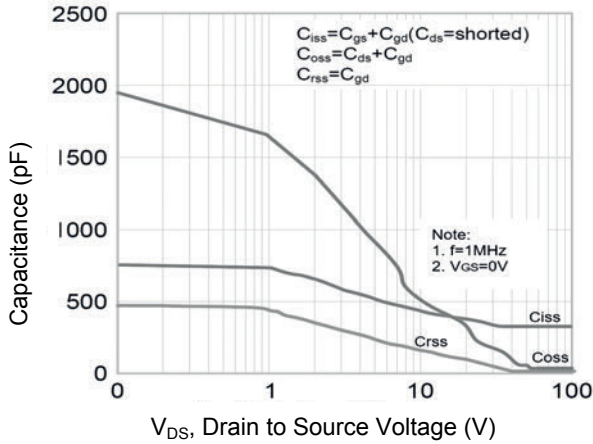


**Figure 5. Normalized  $BV_{DSS}$  vs.  $T_J$**

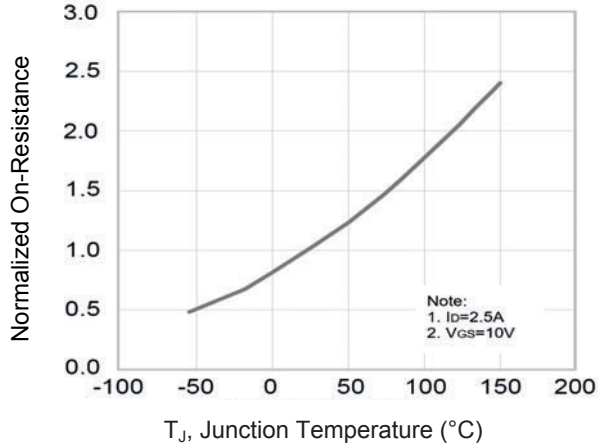


**Figure 6. Gate Charge Characteristics**

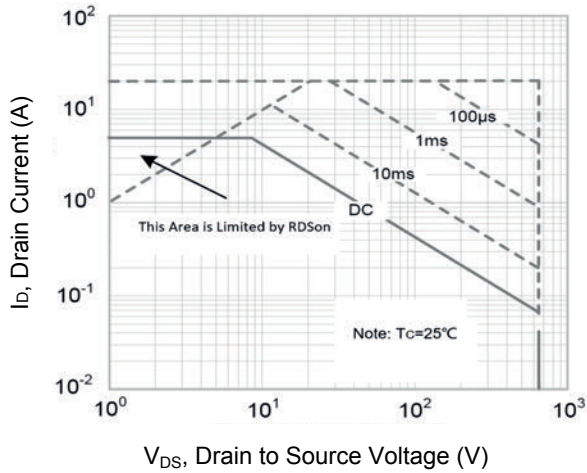
**Typical Electrical and Thermal Characteristic Curves**



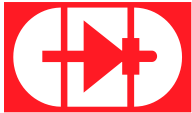
**Figure 7. Capacitance Characteristics**



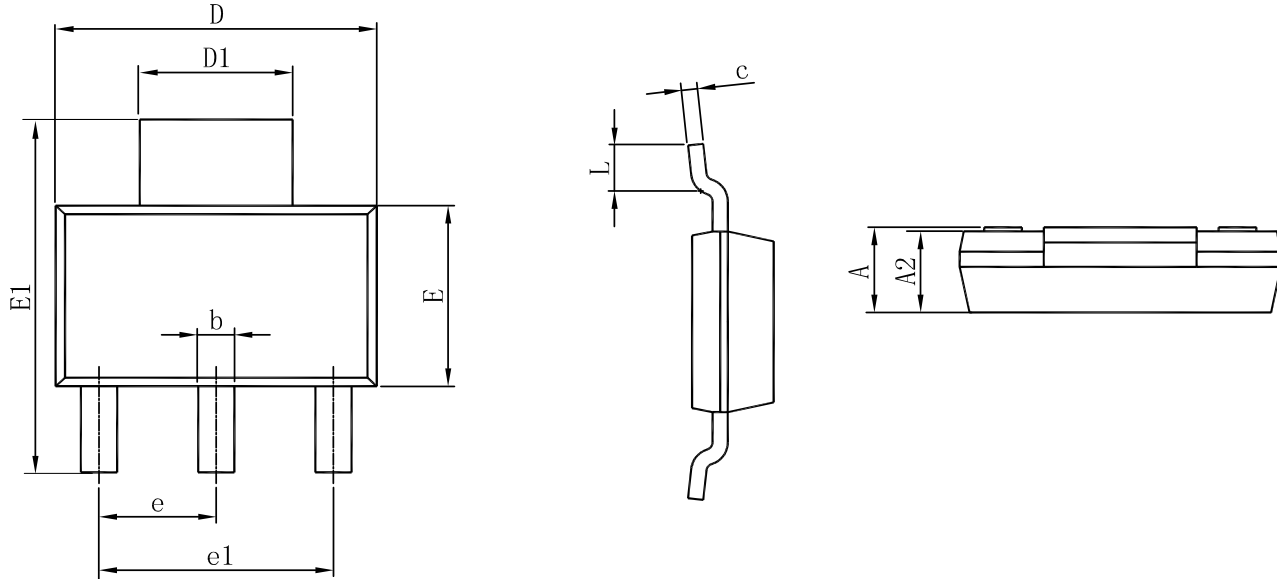
**Figure 8. Normalized  $R_{DS(ON)}$  vs.  $T_J$**



**Figure 9. Safe Operation Area**



## Package Outline Dimensions (SOT-223)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.50	1.80	0.059	0.071
A2	1.45	1.80	0.057	0.071
b	0.60	0.84	0.024	0.033
c	0.20	0.35	0.008	0.014
D	6.20	6.70	0.244	0.264
D1	2.90	3.10	0.114	0.122
E	3.30	3.70	0.130	0.146
E1	6.70	7.30	0.264	0.287
e	2.30 TYP		0.091 TYP	
e1	4.40	4.70	0.173	0.185
L	0.70	1.10	0.028	0.043

## Order Information

Device	Package	Marking	Packaging	SPQ
GSFL65R900	SOT-223	L65R900	Tape & Reel	3,000pcs / Reel