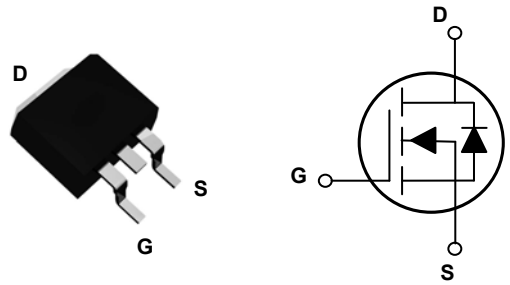


Main Product Characteristics

$V_{(BR)DSS}$	85V
$R_{DS(ON)}$	5.5m Ω (Max.)
I_D	120A



TO-263 (D²PAK)

Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSGT5R585 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	85	V
Gate-to-Source Voltage	V_{GS}	±20	V
Continuous Drain Current, @ Steady-State (T _A =25°C) ¹	I_D	120	A
Continuous Drain Current, @ Steady-State (T _A =100°C)		90	A
Pulsed Drain Current ²	I_{DM}	480	A
Power Dissipation (T _A =25°C)	P_D	160	W
Linear Derating Factor (T _A =25°C)		1.28	W/°C
Single Pulse Avalanche Energy ³	E_{AS}	400	mJ
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.78	°C/W
Junction-to-Ambient (PCB Mounted, Steady-State) ⁴	$R_{\theta JA}$	62.5	°C/W
Operating Junction and Storage Temperature Range	T _J /T _{STG}	-55 to +150	°C

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	85	92.5	-	V
Drain-to-Source Leakage Current	I_{DSS}	$V_{DS}=85V, V_{GS}=0V$	-	-	1	μA
		$T_J=125^\circ\text{C}$	-	-	50	
Gate-to-Source Forward Leakage	I_{GSS}	$V_{GS}=20V$	-	-	100	nA
		$V_{GS}=-20V$	-	-	-100	
Static Drain-to-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=50A$	-	4.2	5.5	m Ω
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.2	3	3.9	V
Dynamic and Switching Characteristics						
Input Capacitance	C_{iss}	$V_{GS}=0V, V_{DS}=40V$ $f=1\text{MHz}$	-	4284	-	pF
Output Capacitance	C_{oss}		-	668	-	
Reverse Transfer Capacitance	C_{rss}		-	18	-	
Total Gate Charge	Q_g	$I_D=50A, V_{DS}=40V,$ $V_{GS}=10V$	-	69	-	nC
Gate-to-Source Charge	Q_{gs}		-	30	-	
Gate-to-Drain ("Miller") Charge	Q_{gd}		-	16	-	
Turn-on Delay Time	$t_{d(on)}$	$V_{GS}=10V, V_{DS}=40V,$ $R_{GEN}=24\Omega, I_D=13.2A$	-	59	-	nS
Rise Time	t_r		-	82	-	
Turn-Off Delay Time	$t_{d(off)}$		-	126	-	
Fall Time	t_f		-	72	-	
Gate Resistance	R_g	$f=1\text{MHz}$	-	1.7	-	Ω
Source-Drain Ratings and Characteristics						
Continuous Source Current (Body Diode)	I_S	MOSFET symbol showing the integral reverse p-n junction diode.	-	-	120	A
Pulsed Source Current (Body Diode)	I_{SM}		-	-	480	A
Diode Forward Voltage	V_{SD}	$I_S=50A, V_{GS}=0V$	-	1	1.2	V
Reverse Recovery Time	T_{rr}	$T_J=25^\circ\text{C}, I_F=20A,$ $di/dt=100A/\mu s$	-	53	-	nS
Reverse Recovery Charge	Q_{rr}		-	0.31	-	μC

Note:

1. Pulse test: pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
2. Repetitive rating; pulse width limited by max. junction temperature.
3. $L=0.5\text{mH}, I_{AS}=40A, V_{DD}=72V, T_J=25^\circ\text{C}$.
4. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

Typical Electrical and Thermal Characteristic Curves

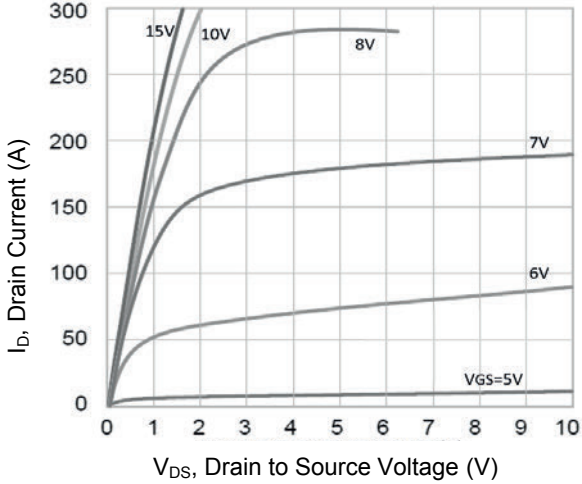


Figure 1. Typical Output Characteristics

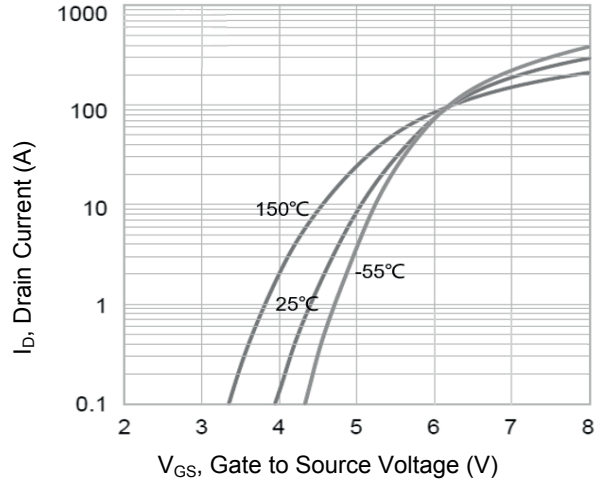


Figure 2. Transfer Characteristics

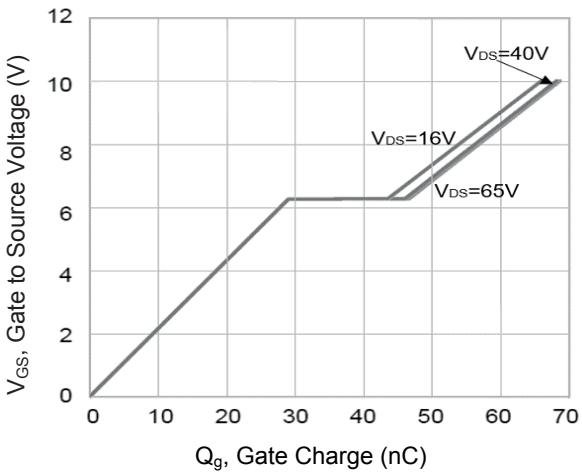


Figure 3. Gate Charge

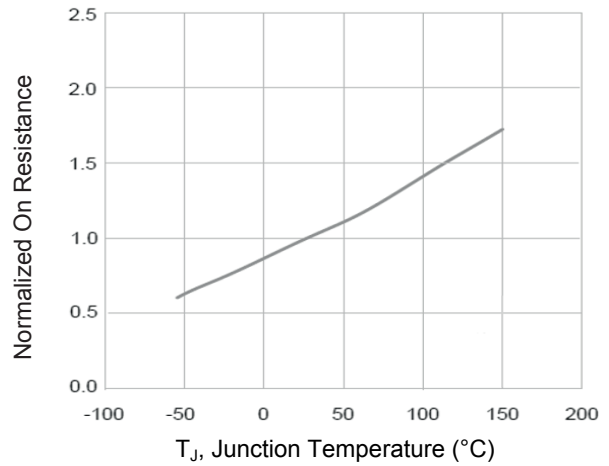


Figure 4. Normalized $R_{DS(ON)}$ vs. T_J

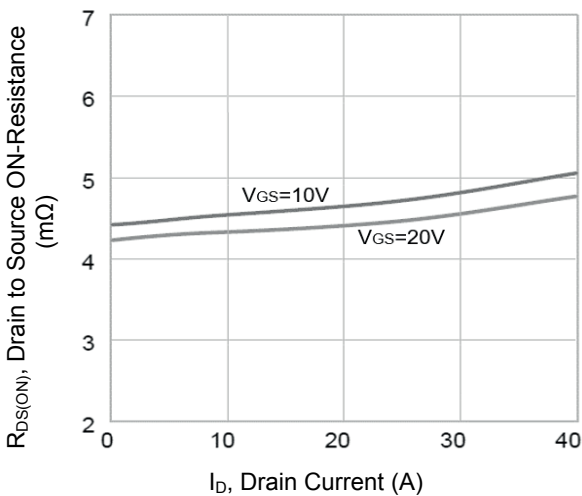


Figure 5. Drain-Source On-Resistance

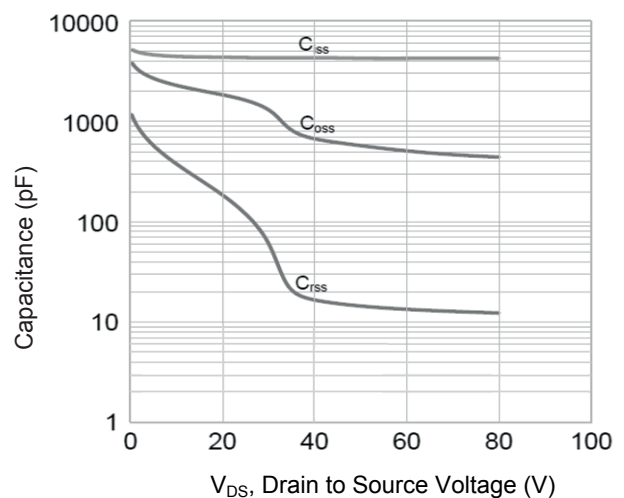


Figure 6. Typical Capacitance vs. Drain to Source Voltage

Typical Electrical and Thermal Characteristic Curves

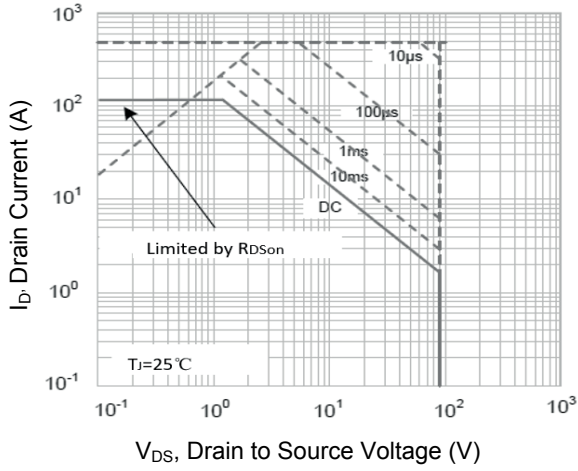


Figure 7. Safe Operation Area

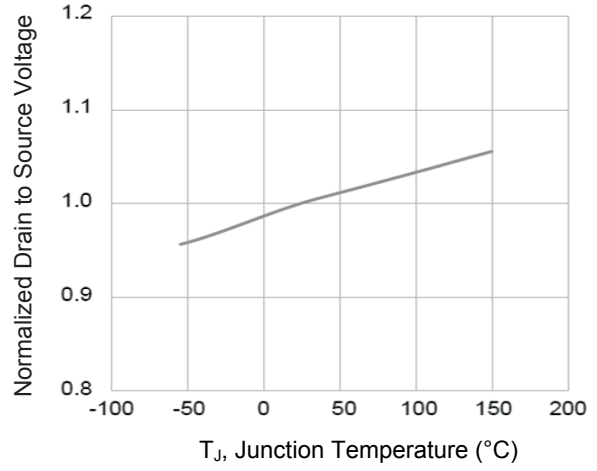
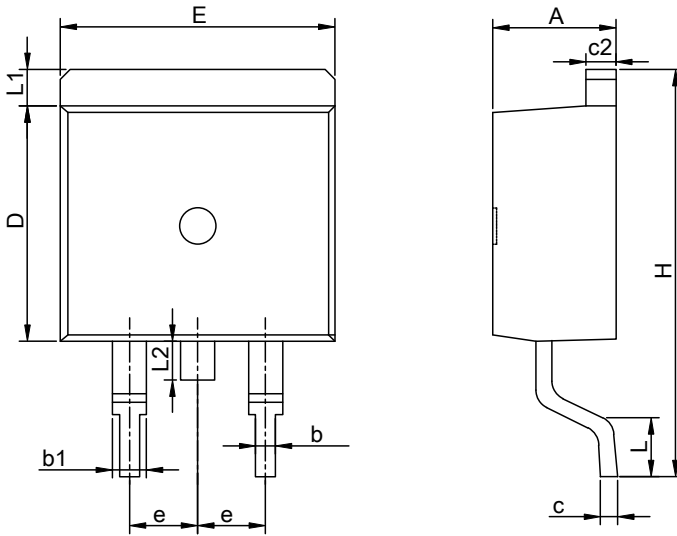


Figure 8. Normalized BVds vs. T_J

Package Outline Dimensions TO-263 (D²PAK)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.30	4.90	0.169	0.193
b	0.70	0.95	0.028	0.037
b1	1.07	1.50	0.042	0.059
c	0.28	0.60	0.011	0.024
c2	1.17	1.37	0.046	0.054
D	8.40	9.35	0.331	0.368
E	9.80	10.45	0.386	0.411
e	2.54 BSC		0.100 BSC	
H	14.70	16.30	0.579	0.642
L	2.00	3.80	0.079	0.150
L1	0.97	1.42	0.038	0.056
L2	-	1.75	-	0.069

Order Information

Device	Package	Marking	Carrier	Quantity
GSGT5R585	TO-263 (D ² PAK)	T5R585	Tape & Reel	800 Pcs / Reel

For more information, please contact us at: inquiry@goodarksemi.com